

JOHN DOE

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ELECTRICAL ENGINEER

Highly motivated, results oriented Senior Researcher, with 5+ year's experience designing and developing low (LV) and high voltage (HV) sections of semiconductor process systems. Demonstrated expertise in data collection, and the statistical analysis of complex engineering obstacles, leading to effective solutions. Particularly accomplished using measurement equipment, oscilloscopes, frequency counters, logic and spectrum analyzers. Well-developed skills in semiconductor process systems, performance optimization, analog and digital electronics design, and troubleshooting. Integral proponent in the innovation of process simplification, enhancement and augmentation. Compelling leader who exceeds all standards. Motivated, passionate and eager to step up into the international semiconductor market.

AREAS OF EXPERTISE

System Design/Integration ♦ Interpreting Information ♦ Calibrating/Refining ♦ Comparing/Evaluating Data
Process Development/Improvement ♦ System Analysis/Improvement ♦ Procedure Development
Consulting ♦ Technology Evaluation ♦ Tracking Progress ♦ Applying Statistical Analysis
Research ♦ Goal Setting ♦ Process Integration ♦ Troubleshooting ♦ Brainstorming
Regulatory Compliance ♦ Employee Relations ♦ Conducting Experiments
Team Leading/Motivating ♦ Problem Solving

EXPERIENCE & ACHIEVEMENTS

DONGBU HITEK, INC, BUCHEON-SI, GYEONGGI-DO, SOUTH KOREA ♦ 01/2005 — 11/2010

Ranked as one of the semiconductor manufacturing industries top 10 foundries, Dongbu Hitek's mission is to deliver the highest quality products backed by superior customer service. \$30B Sales, \$50B Assets, 37,000 employees, with capacity of 94,000 wafers (200mm) monthly.

Senior Researcher ♦ 12/2006 — 11/2010

Presided over 4 team members in the execution of a variety of projects, ensuring compliance with safety regulations and standards. Identified and repaired defects, orchestrated process integrations, and rectified obstacles, devised process improvements and applied simplification methods. Studied parametric data, conducted experiments, and focused on ways to improve and enhance production.

- ♦ Developed CMOS Transistor for voltage domain, so end users can operate LCD driver and integrated circuit (IC) products.
- ♦ Conducted experiments on low and high voltage areas simultaneously in support of constant voltage changes by companies, in order to develop products ahead of time.
- ♦ Orchestrated mask drawing works for realizing test patterns.
- ♦ Honed channel length, STI length & depth, oxide thickness and implant dosage.
- ♦ Attained mastery use of TCAD tools.

Process Integration Engineer ♦ 01/2005 — 12/2006

Modified processes and established means of improving output, boosted ratio quality, carried out process augmentation, and schematization.

- ♦ Monitored process flow and system status, using advanced statistic analysis tools.
- ♦ Resolved operation problems through collaboration with product and design teams, and communicating to customers.
- ♦ Earned Six Sigma green belt.

KOREA UNIVERSITY, SEOUL, KOREA ♦ 2003 — 2005

Graduate Research Assistant

- ♦ Completed process & equipment researcher E-beam course, semiconductor process education, micro-system technology, and application education at the ISRC (Inter University Semiconductor Research Center).
- ♦ Participated in electrochemical bio-sensor education, at Industrial Research Institute, Sogang U.

EDUCATION

Bachelor Degree in Control and Instrumentation Engineering

Korea University, Seoul
02/2003, GPA 3.58 out of 4.5

Master Degree in Electronics & Electrical Engineering

Korea University, Seoul
02/2005, GPA 4.14 out of 4.5

ADDITIONAL HIGHLIGHTS

Computer Knowledge

- ♦ C++ Language
- ♦ Basic
- ♦ Linux
- ♦ Window XP
- ♦ TCAD
- ♦ Cadence
- ♦ LAKERS
- ♦ LabVIEW
- ♦ SPICE

Presentations

- ♦ Korea EMS Conference
- ♦ Korean Institute of Electrical Engineers
- ♦ Korean Conference on Semiconductors and, numerous others

Languages

- ♦ English
- ♦ Korean

Current Status

- ♦ Since leaving Dongbu Hitek, making preparations to continue education, and earn PhD.

Publications & Patents

- ♦ Issued (under Dongbu Hitek, Inc). various patents for developing new methodologies relating to semiconductors
- ♦ Selection of publications produced while participating in graduate research coursework.